

FIG. 1

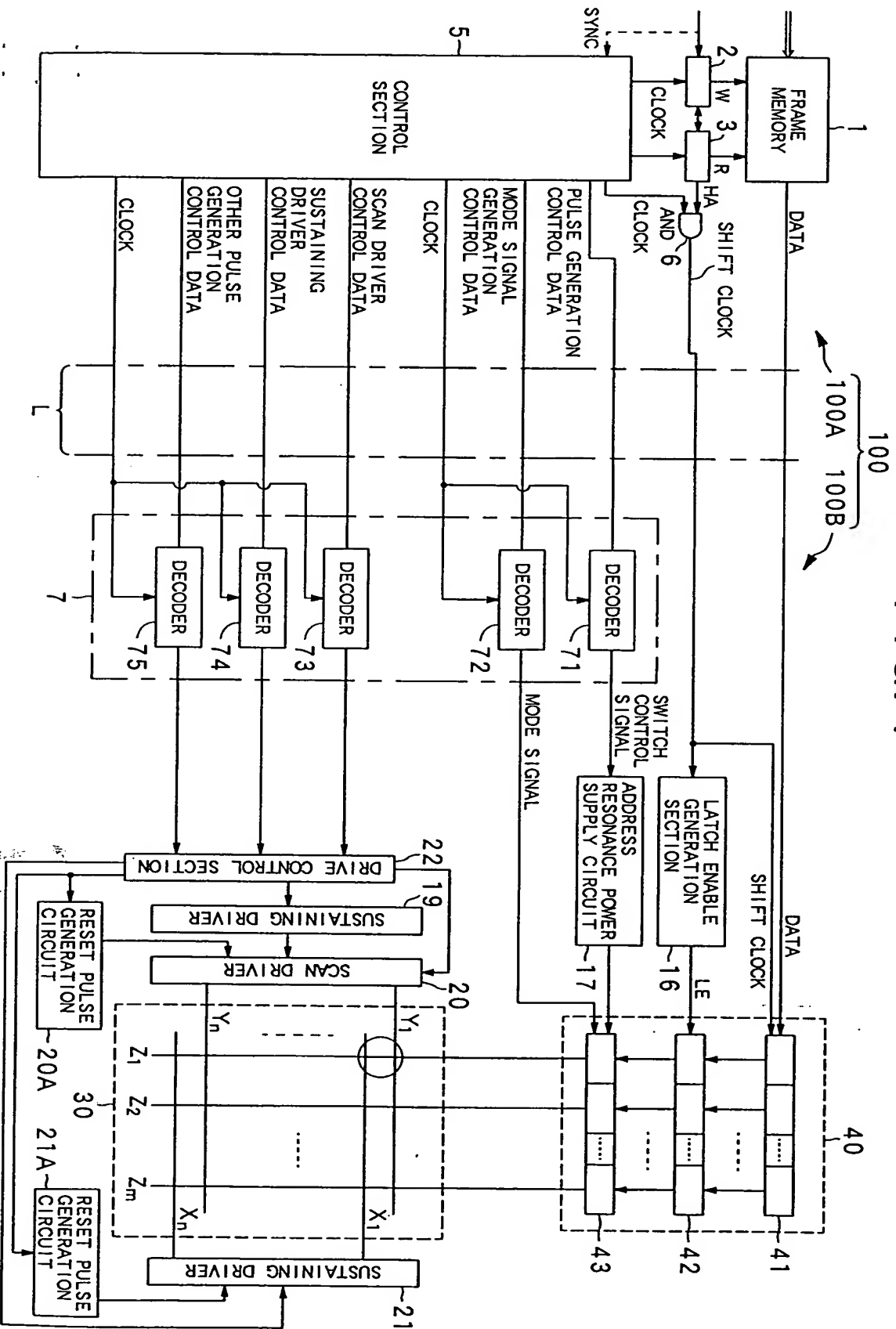


FIG. 2

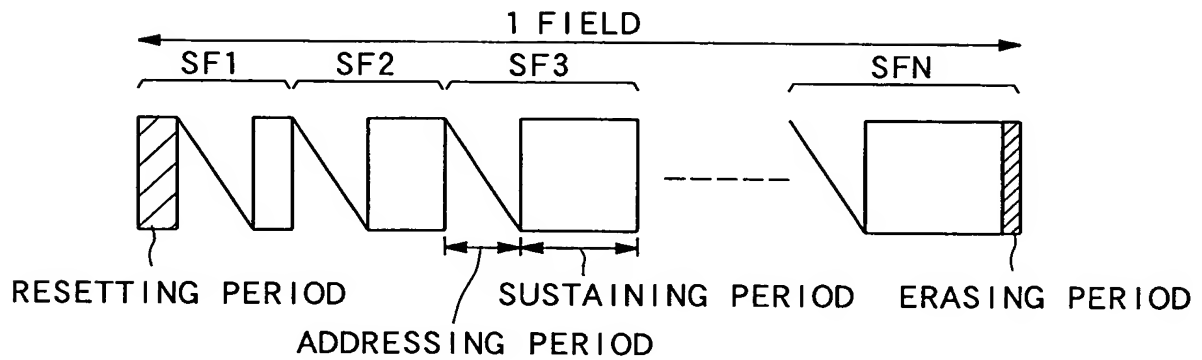


FIG. 3

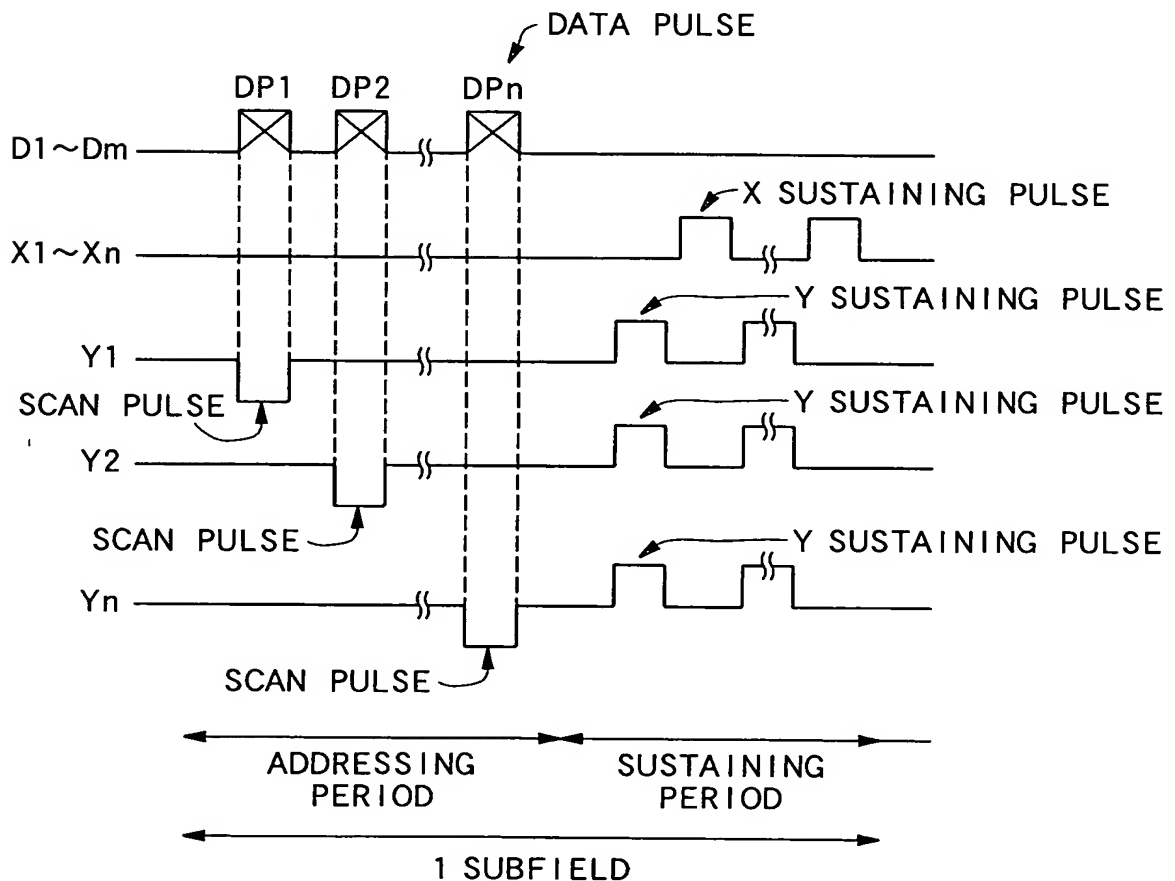


FIG. 4

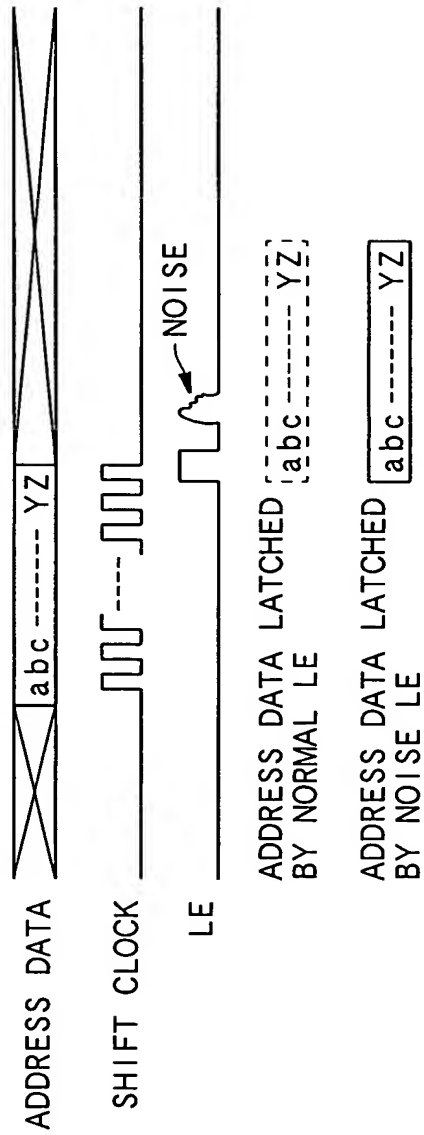


FIG. 5

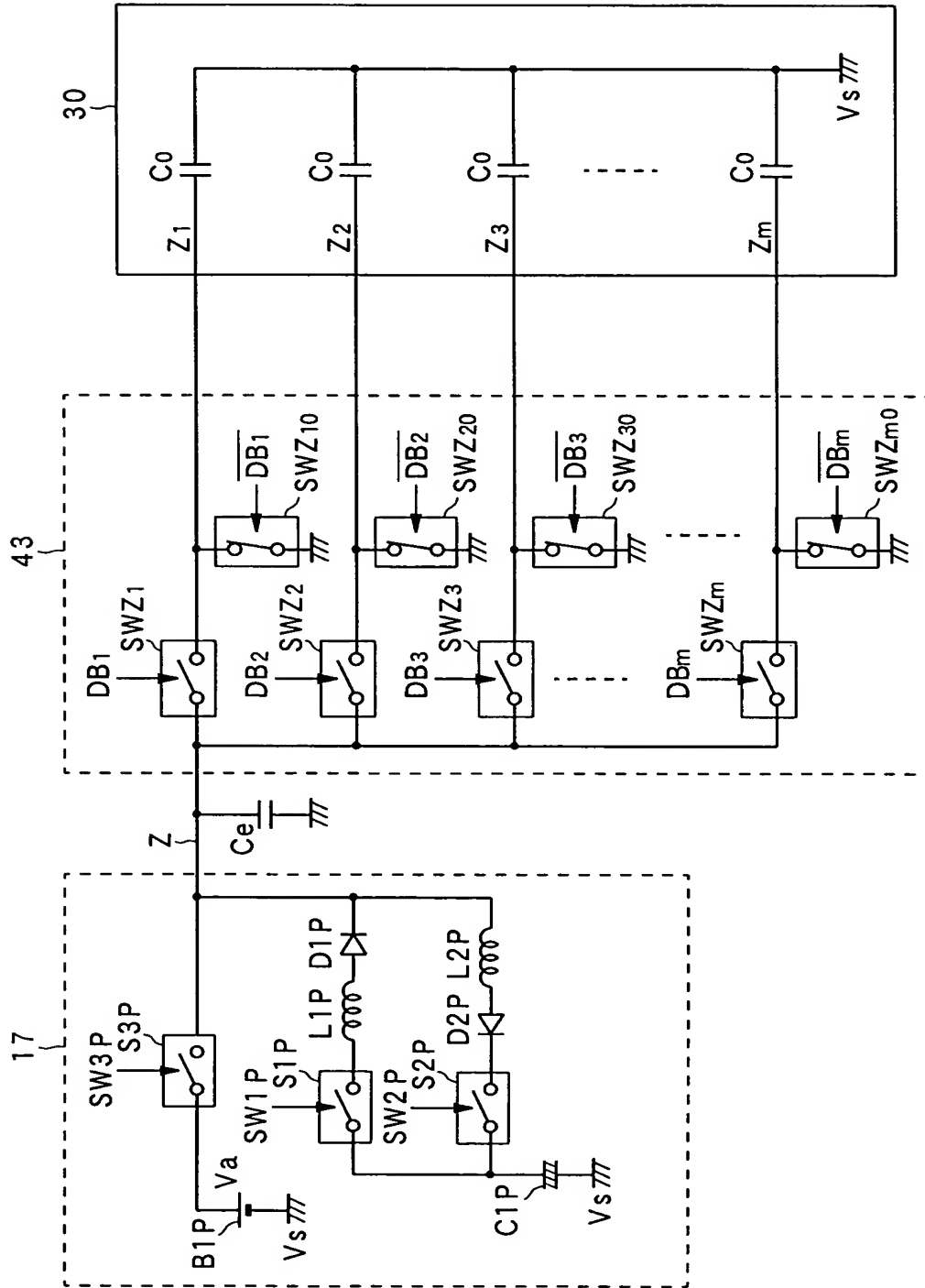
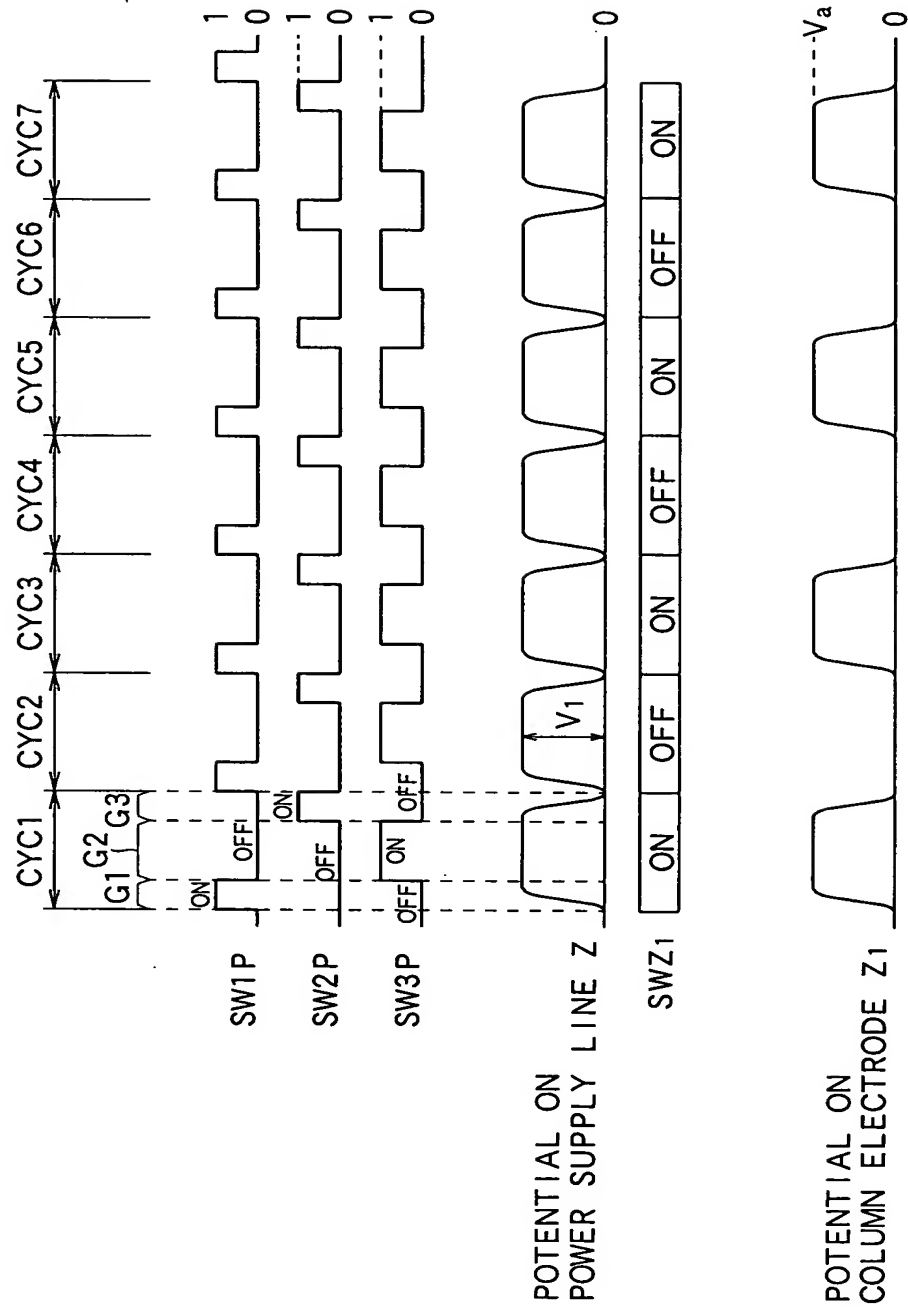


FIG. 6



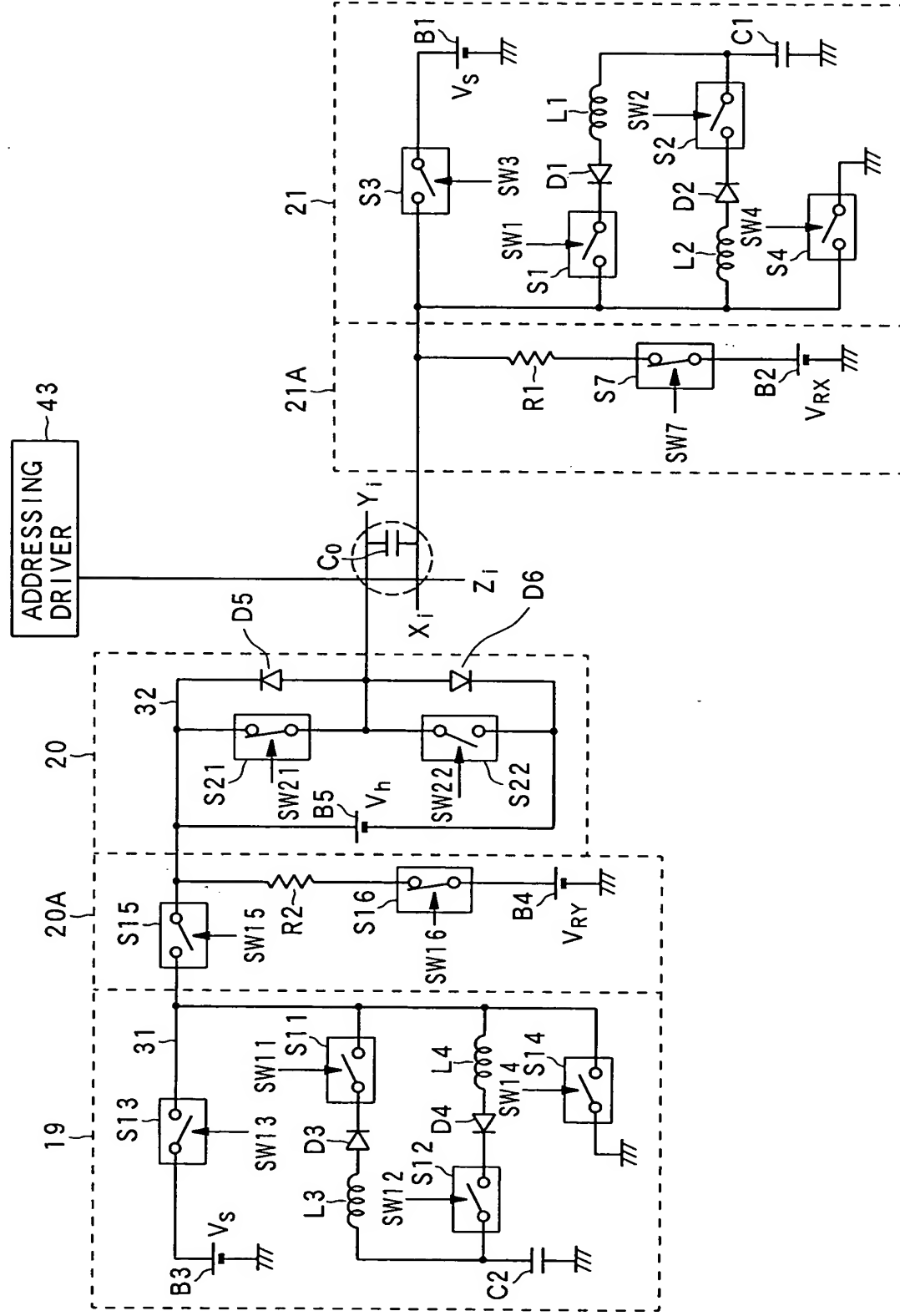


FIG. 8

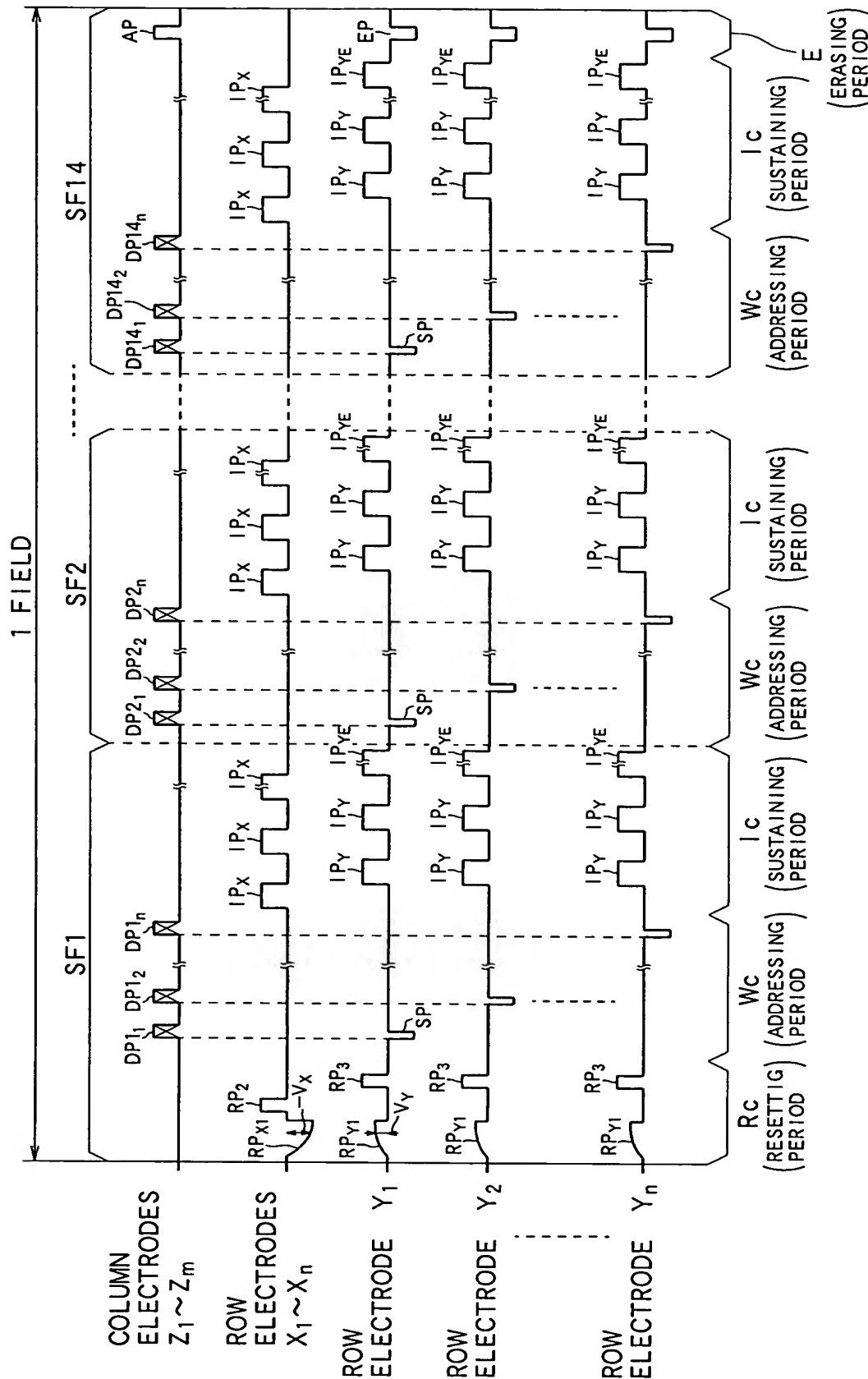


FIG. 9

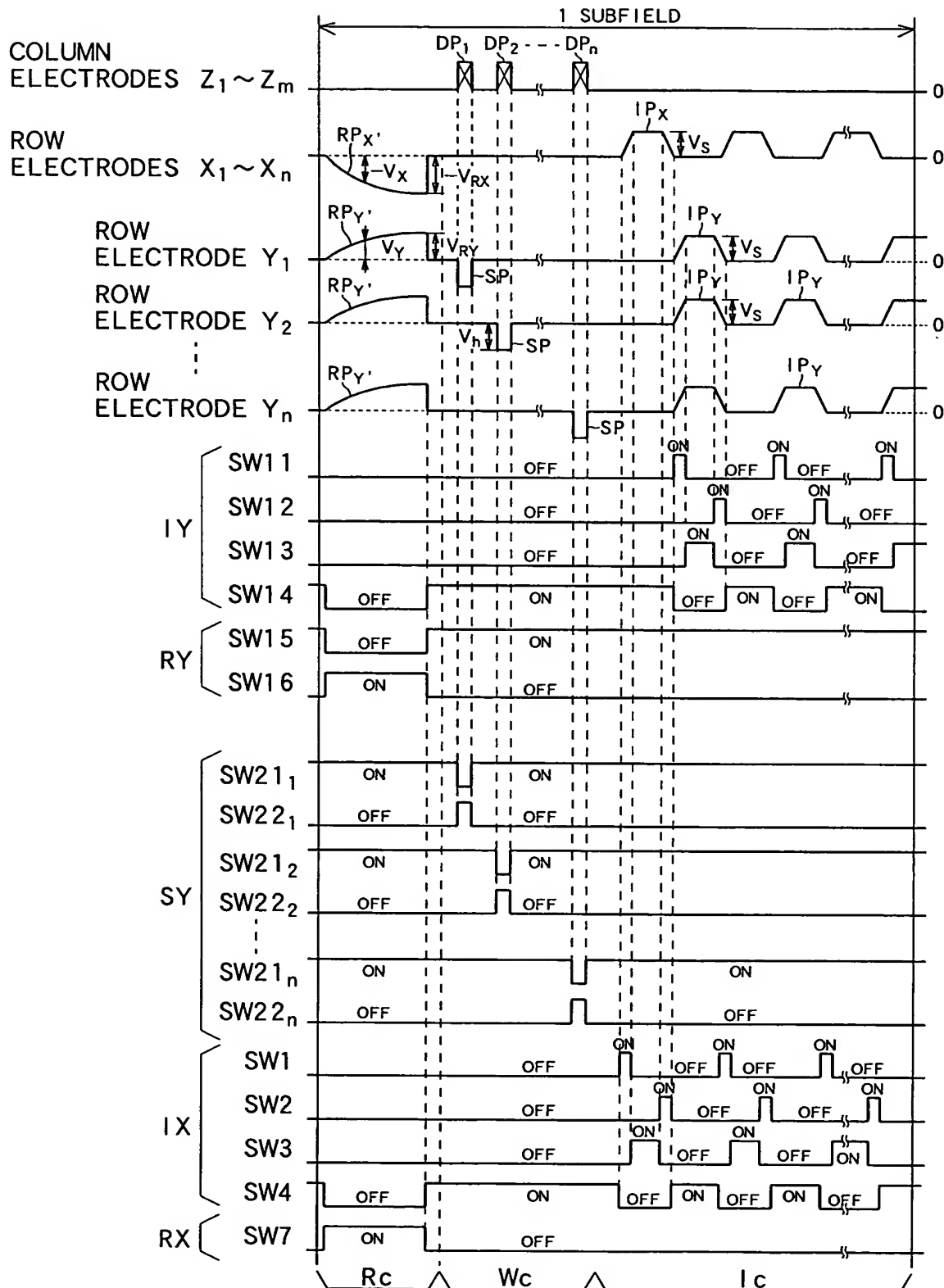


FIG.10A

INPUT	OUTPUT		
	SW1P	SW2P	SW3P
00	0	0	0
01	1	0	0
10	1	0	1
11	0	1	0

FIG.10B

INPUT	OUTPUT		
	A	B	C
00	1	1	0
01	0	0	1
10	0	0	0
11	0	1	0

FIG.10C

INPUT	OUTPUT			
	SW1	SW2	SW3	SW4
000	0	0	0	0
001	0	0	0	1
010	1	0	0	0
011	1	0	1	0
100	0	0	0	1

FIG.11

